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Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE
International Symposium on , Volume: 5 , 6-9 May 2001
Page(s): 307 -310 vol. 5

[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) **IEEE CNF**

2 Beyond SPICE with Saber and MAST

Mantooth, H.A.; Vlach, M.;
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International Symposium on , Volume: 1 , 3-6 May 1992
Page(s): 77 -80 vol.1

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Vlach, M.;
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[\[Abstract\]](#) [\[PDF Full-Text \(664 KB\)\]](#) **IEEE CNF**

4 Computer oriented formulation of equations and analysis of switched-capacitor networks

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5 FDNR switched-capacitor filters insensitive to parasitic capacitances

Faruque, S.; Vlach, M.; Vlach, J.; Singhal, K.; Viswanathan, T.;
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Sep 1982
Page(s): 589 -595

[\[Abstract\]](#) [\[PDF Full-Text \(832 KB\)\]](#) **IEEE JNL**

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[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) **IEEE CNF**

2 VHDL-AMS-a hardware description language for analog and mixed-signal applications

Christen, E.; Bakalar, K.;
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3 VHDL 1076.1-analog and mixed-signal extensions to VHDL

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9 Poles, zeros, and their sensitivities in switched-capacitor networks

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